ABSTRACT

An image encoding system includes a processor and a memory device coupled to the processor. The processor is configured to receive input data containing image information, to generate encoded data based on a frequency domain transform of the input data, and to selectively generate an encoded reference based on a frequency domain transform of the input data. The system also includes a memory device coupled to the processor. The processor is further configured to store the generated encoded reference in the memory device when no encoded reference is available in the memory device and generate residue data representing the difference between the encoded data and the stored encoded reference when the encoded reference is available in the memory device.